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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Hiroyuki WATANABE et al.

Serial Number: Not Yet Assigned

Filed: May 27, 1999

For: SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREOF

JC594 U.S. PTO
09/320271
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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

May 27, 1999

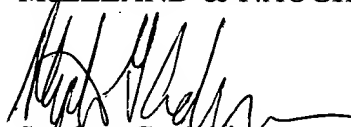
Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,
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Enclosures: PTO-1449; References (4).